JOB VACANCY

ABOUT VLC PHOTONICS

VLC Photonics was born in 2011 as a technological spin-off company from the Technical University of Valencia, and since October 2020 belongs to the Hitachi High-Tech Corporation group.

The company now focuses its solutions on the photonic integration technology. It is a world pioneer in the design house business model, providing all kind of services related to this technology: engineering consultancy, photonic design, photonic testing, and brokerage for manufacturing and packaging subcontracting. Check our website for more info.

SENIOR PHOTONIC INTEGRATED CIRCUIT DESIGNER

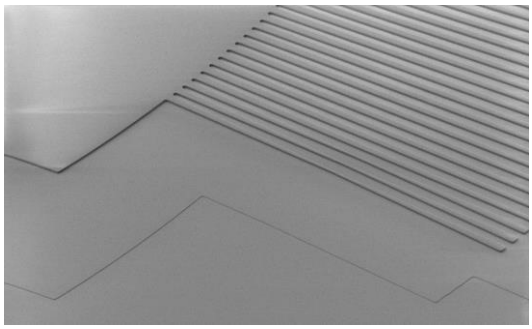
VLC Photonics is a young and very dynamic company, now searching for a new member to be added to its growing team.

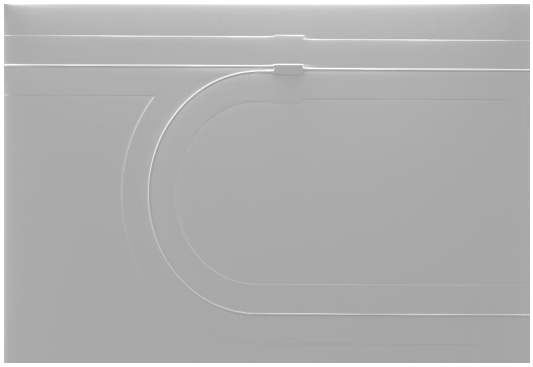
REQUIRED BACKGROUND/EDUCATION

Engineering degree on Physics, Telecommunications, Microelectronics/RF, Optics/Photonics, or similar.

REQUIRED SKILLS

- Advanced knowledge of optics and photonics technology. Master or Ph.D. in Optics/Photonics, or in Semiconductors.
- Advanced Photonic Integrated Circuit (PIC) design skills (Synopsys, Photon Design, IPKISS/Luceda, Lumerical, VPI, Klayout, etc.)
- Minimum of 3+ years hands-on experience in integrated optics design and simulation.
- Programming knowledge (Python, C++ ...)
- High level of English (fluent B2 minimum). English interview.
- Very pro-active, solution oriented and versatile character.
- Organized, capable of dealing with tight deadlines.
- Good analytic and communication skills.





GROSS ANNUAL SALARY

Depending on candidate's adequacy.

OTHER BENEFITS

- Access to UPV campus facilities (sports complex, library, parking, medical center, etc.)
- Close access to public transport (tram, train, rental bikes)
- Very flexible worktime schedule.

LOCATION

VLC Photonics offices, Valencia, Spain (map)

STARTING DATE

As soon as possible.

VALUABLE SKILLS

- Practical experience on an optical laboratory, measurement equipment and tools, PIC characterization, and test skills.
- PIC fabrication/packaging knowledge.
- Matlab/Octave know-how.
- Experience in customer and project management.
- Availability for international travelling.
- Experience in photonic integration technology and its ecosystem.
- Any other extra language (e.g., French, German, Dutch, Chinese, Russian, Japanese). Spanish not strictly needed but recommended.
- *Microsoft Office* suites, medium-advanced knowledge.
- Good international networking skills and available professional network. Having lived abroad is considered valuable.
- Ambition for growth and expansion.

POSITION RELATED TASKS

- Lead and coach a PIC engineering team.
- Modelling, simulation, design and layout of photonic components (both active and passive) and circuits in Silicon photonics, indium phosphide, Silicon nitride, Silica, etc.
- Foundry and software provider interfacing.
- Supervision of technical tasks performed by technicians, interns and/or other less skilled technical personnel.
- Collaboration in the preparation of customer and R&D proposals, and project tasks.
- Customer interfacing: teleconferences and meetings, project management.
- Support recruiting new employees.
- Maintain and identify needs with regards to infrastructure.
- Plan and monitor resources and activities within the PIC Design team.
- Contribute to the continuous improvement of processes relevant to the team, e.g. engineering and documentation best practices and use of tools.

VLC Photonics offers a stimulating and varied work environment in a young but growing high-tech business start-up, located at an attractive international city in the Mediterranean coast (300 sunny days a year!). Cost per living in Valencia is very affordable compared to other Spanish or European large cities (check). Additionally, VLC Photonics offers continuous education and training in all technical activities, and provides free or low-cost access to certified courses in many areas through the Permanent Training Center (CFP) of the UPV.

Interested candidates please forward the CV and presentation letter at careers@vlcphotonics.com for evaluation, indicating in the subject "**Senior Photonic Integrated Circuit Designer position**".